

09/936267

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Sheet 1 of 1

Form PTO-1449
(REV. 8-83)US Dept. of Commerce
PATENT & TRADEMARK OFFICEATTY DOCKET NO.
110602APPLICATION NO.
U.S. National Stage of
PCT/JP00/01471

INFORMATION DISCLOSURE STATEMENT

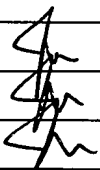
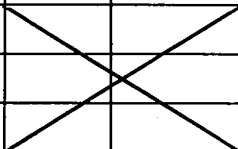
(Use several sheets if necessary)

APPLICANTS
Masatoshi ISHIKAWA, Haruyoshi TOYODAFILING DATE
September 10, 2001

U.S. PATENT DOCUMENTS

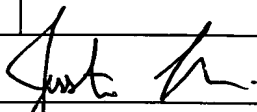
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS
	1	JP A 62-295174	12/22/1987	Japan		
	2	JP A 6-274467	09/30/1994	Japan		
	3	JP A 10-145680	05/29/1998	Japan		

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

EXAMINER



DATE CONSIDERED

9/15/04

Examiner: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.



Sheet 1 of 1

Form PTO-1449 (REV. 8-83)		US Dept. of Commerce PATENT & TRADEMARK OFFICE		ATTY DOCKET NO. 110602		APPLICATION NO. 09/936,267	
INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)				APPLICANTS Masatoshi ISHIKAWA et al.			
				FILING DATE September 10, 2001		GROUP 2614	
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	
	1	09/807,039 (Atty. docket# 109147)	04/09/2001	Toyoda et al.			
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	
	2	JP A 63-113659 (w/abstract)	05/18/1988	Japan			
	3	JP A 6-195480 (w/abstract)	07/15/1994	Japan			
	4	JP A 6-20070 (w/abstract)	01/28/1994	Japan			
	5	JP A 8-235149 (w/abstract)	09/13/1996	Japan			
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)							
	6	"Artificial retinas-fast, versatile image processors", Kazuo Kyuma et al., Nature, vol. 372, November 10, 1994.					
	7	"Computing Motion Using Analog and Binary Resistive Networks", James Hutchinson et al., Computer, Vol. 21, March 1988, pp. 52-64, IEEE.					
	8	"An Object Position and Orientation IC with Embedded Imager", David L. Standley, Solid State Circuits, Vol. 26, No. 12, Dec. 1991, pp. 1853-1859, IEEE.					
EXAMINER 				DATE CONSIDERED 9/15/01			
Examiner: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							